

IN THE CLAIMS:

Please amend Claim 1 as shown below. The claims, as pending in the subject application, now read as follows:

1. (Currently amended) An image processing apparatus comprising:  
a sensor including a plurality of pixels each including at least a light receiving element, an amplifier element and a selecting element, wherein an electrical signal from said light receiving element is output to a common output line through said selecting element;  
a scanning circuit for reading out the electrical signal in time sequence from the plural pixels; and  
a drive circuit which supplies pulses to said selecting element,  
wherein said drive circuit is [[so]] arranged to drive said scanning circuit so that said scanning circuit supplies a first pulse having a first pulse width to said selecting element of a pixel to be read out, when a first resolution is selected, and supplies the first pulse to said selecting element of the pixel to be read out and a second pulse having a second pulse width smaller than the first pulse width to said selecting element of a pixel to be thinned out, when a second resolution lower than the first resolution is selected.
2. (Original) An apparatus according to claim 1, wherein when the second resolution is selected, said drive circuit supplies the first pulse in every other pulse or in every plurality of pulses.

3. (Previously presented) An apparatus according to claim 2, further comprising a signal processing circuit which performs image processing on the basis of signals which are read out by supplying the first pulse from said scanning circuit.

4. (Original) An apparatus according to claim 2, wherein said sensor is formed on the same semiconductor chip, and a plurality of said sensors are mounted on a mount board.

5. (Previously presented) An apparatus according to claim 1, wherein said light receiving element, said amplifier element and said selecting element are a photodiode, an amplifier transistor and a selecting transistor, respectively, each of said plurality of pixels further has a reset switch for resetting an input portion of said amplifier transistor, said amplifier transistor amplifies the signal from said photodiode and outputs the amplified signal, and said selecting transistor is arranged to selectively read out the signal from said amplifier transistor.

6. (Original) An apparatus according to claim 1, further comprising a control circuit for switching between the first resolution and the second resolution.

7. (Original) An apparatus according to claim 1, further comprising a light source for irradiating light on said sensor, and a transport member for moving an original and said sensor relative to each other.